



Supporting the Future of Northern Semiconductor Industries 1-Day Conference – 6th June 2024

Next generation electronics, using advanced materials, are opening vast new horizons that impact on every facet of society. North East England is at the vanguard of many of these and home to over 30 companies and organisations driving multiple revolutions. Already over 1800 people are employed locally in the sector with growth forecasts expected to generate a further 2700 jobs by 2027.

This conference aims to raise the profile of our regions semiconductor industry, highlight the industrial and academic work being undertaken by the sector in the North East, and to support the skills required for it to develop further by showcasing the work of local PhD students and giving an opportunity for students to network with industry. This conference includes speakers from academia, DSIT and members from both the North East Advanced Material Electronics cluster as well as other invited speakers. The conference sessions and a selection of speakers are below.

Session 1 (Overview of the NEAME Cluster)

An opportunity to hear about the latest manufacturing and design work being undertaken by companies in the North East. An ideal opportunity for students and early career engineers to meet with industry.

Session 2 (Regional and UK Semiconductor Policy)

An overview of future directions for supporting the semiconductor industry in the North East as well as opportunities for investing in the sector.

Session 3 (Future Directions for Semiconductors and Skills)

A session on how to support start ups and recruitment into the industry, as well as training opportunities on offer for developing skills for cleanroom operation and semiconductor fabrication.

Session 4 (Future Directions for NE Semiconductors)

Talks covering the current academic research being undertaken in North East Universities.

Registration Fees:

NEIMME Members: £65 Non-Members: £70 Students: £35

Conference Website Conference Registration Page

To support any aspect of our event, or to support our Institute in general. Please contact **office@mininginstitute.org.uk** to express an interest to our Treasurer.



Programme:

09:00 - Registration

09:00-12:00 - Session 1 (Overview of the NEAME Cluster)

Conference Opening - NEIMME President

Elaine Scott - Space North East England

Introduction to NEAME Cluster

Keynote Semiconductor Presentation

Scott White - Executive Director, Strategic Initiatives - Pragmatic

Revolutionising Semiconductor Technology & Manufacturing

Andrew Stokes - Projects and Systems Manager - INEX

Overview of INEX

Dr Tudor Williams - Director of Technology - Filtronic

Overview of Filtronic

Jim Mayock - Director - VIPER RF

Overview of Viper RF

10:30 - 11.00 Break

11.00-12:00 - Session 2 (Regional and UK Semiconductor Policy)

Nicky Athanassopoulou – Institute for Manufacturing (IfM) – Cambridge University

Overview of the UK Semiconductor Infrastructure Initiative

Simon Johnson - Chief Technologist - Photonics & Electronics Technology Centre - Centre for Process Innovation

Future Technologies for Semiconductor Integration

Guy Currey - Director - Invest North East England

Investing in the North East

Andy Kerr - Head of Economic Development at Durham County Council

Strategic Growth for Semiconductors in County Durham (TBA)

Andy Sellars - South Wales Semiconductor Cluster

Refreshing the North East Advanced Material Electronics (NEAME) economic study

Richard Duffy - Senior Policy Advisor, Semiconductors - Department for Science, Innovation and Technology

UK Semiconductor Strategy

12:30-13:30 - Lunch (Wood Hall)

13:30-15:00 - Session 3 (Future Directions for Semiconductors and Skills)

Neil Dickens - IC Resources

Sensational Semiconductor Skills and Start-Ups

Nacent Semiconductor

TBA - Engineering Quantum Technologies

Oxford Instruments

Future of semiconductor Processing Equipment and Techniques

Derrick Holliday - DER-IC North East

Driving the Electric Revolution - Racing Ahead

Rob Farr - University of Leeds

Research Technical Professional (RTP) Upskilling for Semiconductor Fabrication

Steve Reynolds - NPL

Future needs in standard testing and specifications for emerging materials and technology

15:00 - 15:30 Break

15:30-17:00 - Session 4 (Future Directions for NE Semiconductors)

Northumbria University

Semiconductors for Space Applications

Emilia Russell - Durham University

Designing Location Sensitive Photodetectors Based on Strained 2D materials

Rui Fang – Durham University

2D Metasurface Structural Colour

17:00-17:30 - Tea/Coffee, Networking, and Wrap-Up